Fig. 1

(Prior Art)

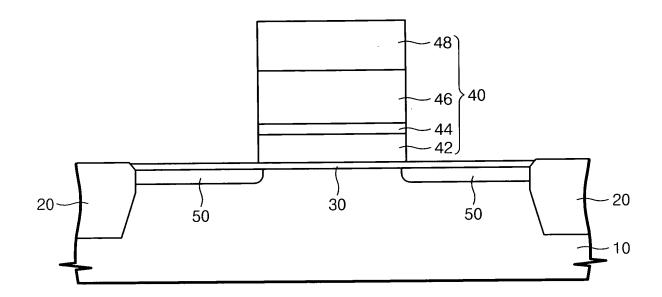
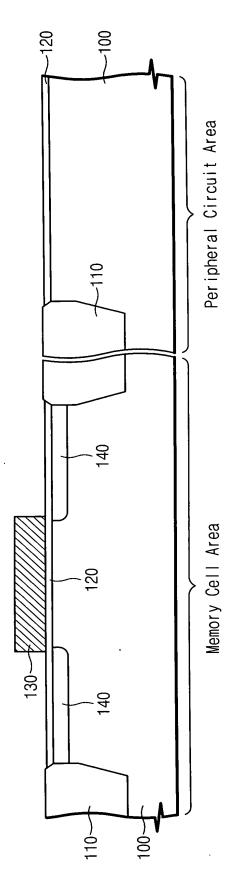
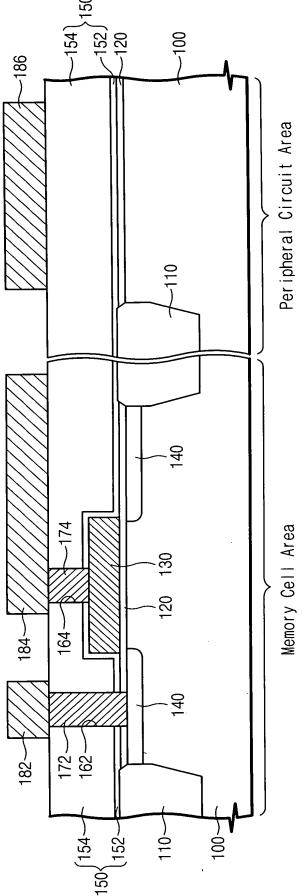


Fig. 2



-186 184

Fig. 3

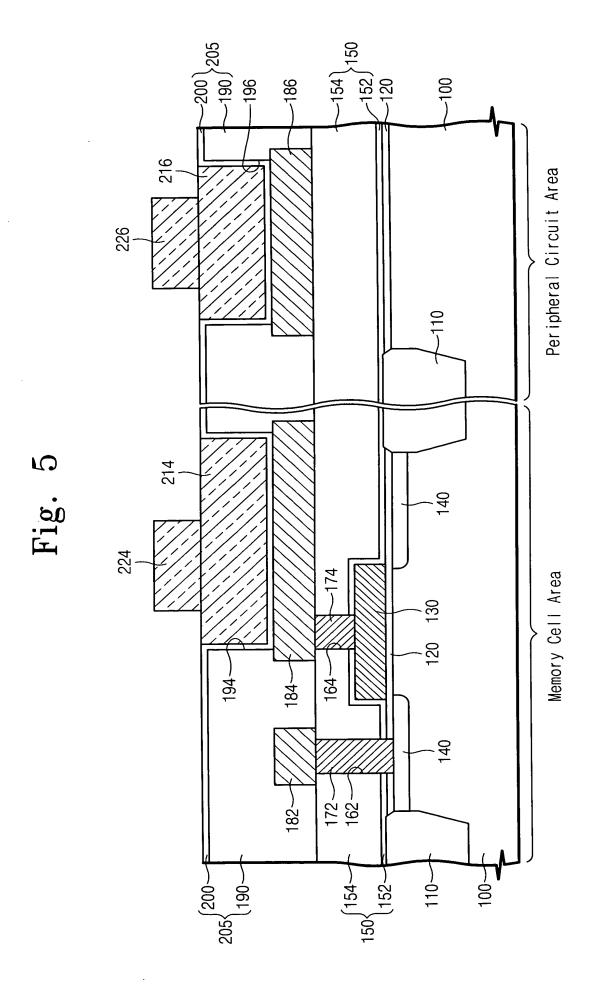


-200 -190 -196-154 -152 -120 -186 100 140 -174 120 130 194 164 184~ 140 172~ 162~ 1001 205 200 (154— (152— <del>1</del>0 <del>1</del>0

Peripheral Circuit Area

Memory Cell Area

Fig. 4



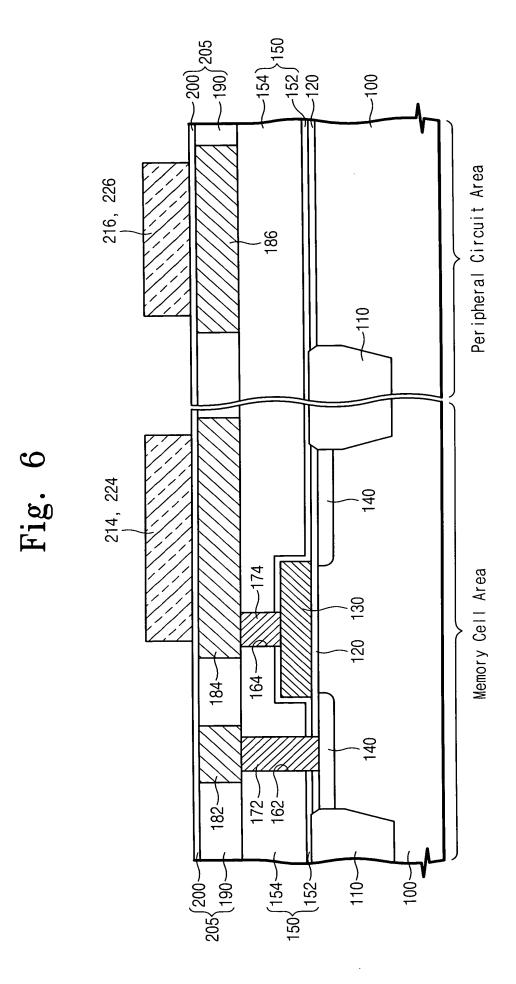


Fig. 7A

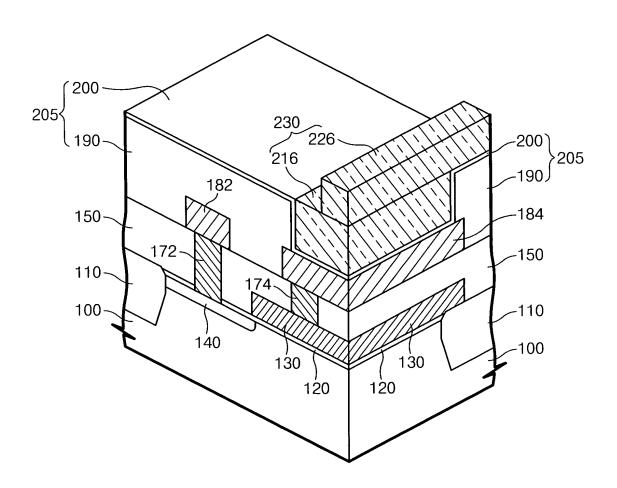


Fig. 7B

